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Attorney's Docket No.: 002986, P034

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:

Nils Endric Schubert

Application No: 09/724,839

Filed: November 28, 2000

For: Design Instrumentation Circuitry

Examiner: Yolanda L. Wilson

Art Unit: 2184

Confirmation No.: 6149

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450CROSS-REFERENCE UNDER 37 C.F.R. §1.78(a)(2) TO RELATED
APPLICATIONS

Sir:

Pursuant to 37 C.F.R. §1.78(a)(2), applicants hereby cross-reference the following related patent applications, which is assigned to the same assignee as the present application:

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09/724,839

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02986.P034

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- (1) U.S. Patent Application No. 09/724,702, filed on November 28, 2000, now U.S. Patent No. 6,581,191 B1, entitled, Hardware Debugging In A Hardware Description Language, by Nils Endric Schubert, John Mark Beardslee and Douglas L. Perry.
- (2) U.S. Patent Application No. 10/406,732, filed on April 2, 2003, entitled, Hardware Debugging In A Hardware Description Language, by Nils Endric Schubert, John Mark Beardslee and Douglas L. Perry.
- (3) U.S. Patent Application No. 10/377,907, filed on February 28, 2003, entitled, Hardware-based HDL Code Coverage and Design Analysis, by Nils Endric Schubert, John Mark Beardslee, Gernot Heinrich Koch and Ewald John Detjens.
- (4) U.S. Patent Application No. 09/724,585, filed on November 28, 2000, entitled, Method and System For Debugging An Electronic System, by John Mark Beardslee, Nils Endric Schubert and Douglas L. Perry.
- (5) U.S. Patent Application No. 10/212,128, filed on July 31, 2002, entitled, Method and System For Debugging An Electric System Using Instrumentation Circuitry and A Logic Analyzer, by Nils Endric Schubert, John Mark Beardslee, Gernot Heinrich Koch and Olaf Poepppe.

- (6) U.S. Patent Application No. 10/456,768, filed on June 6, 2003, entitled, Hardware/Software Co-Debugging In A Hardware Description Language, by Nils Endric Schubert, Kenneth S. McElvain, John Mark Beardslee and Mario Larouche.
- (7) U.S. Patent Application No. 10/210,509, filed on July 31, 2002, entitled, Method and User Interface For Debugging An Electronic System, by Nils Endric Schubert, John Mark Beardslee, Gernot Heinrich Koch and Olaf Poeppel.
- (8) U.S. Patent Application No. 09/724,840, filed on November 28, 2000, now U.S. Patent No. 6,618,839 B1, entitled, Method and System For Providing An Electronic System Design With Enhanced Debugging Capabilities, by John Mark Beardslee, Nils Endric Schubert, and Douglas L. Perry.
- (9) U.S. Patent Application No. 10/455,917, filed on June 6, 2003, entitled, Enhanced Hardware Debugging With Embedded FPGAS In A Hardware Description Language, by Nils Endric Schubert, Kenneth S. McElvain, John Mark Beardslee and Mario Larouche.

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 1/15/04

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 Our Docket No.: 2986P034 Number of pages 5 including this sheet.
 Application No.: 09/724,839 Filing Date: 11/28/2000
 Docket Due Date(s): _____

Enclosed are the following documents:

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<input type="checkbox"/> Appeal Brief (in triplicate) (_____ pgs)	<input type="checkbox"/> Notice of Appeal
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Cross-Reference Under 37 C.F.R. 1.78(a)(2) To Related Applications (4) pages	

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